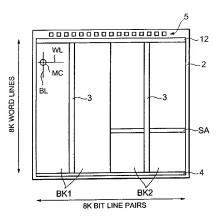
JP920030200US1 Sunaga, et. al.

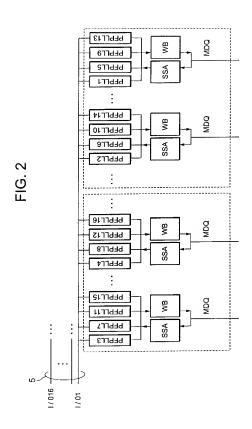
1/14

FIG. 1

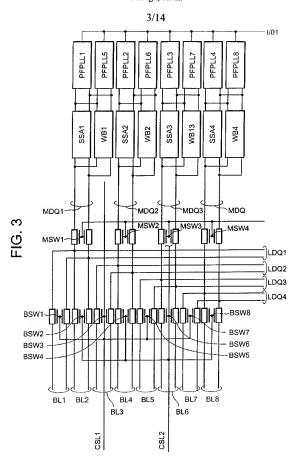


<u>10</u>

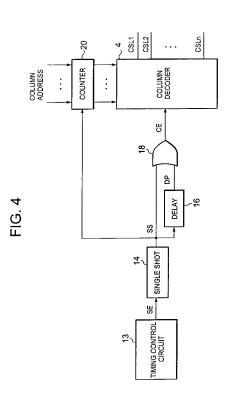
2/14



JP920030200US1 Sunaga, et. al.



4/14



JP920030200US1 Sunaga, et. al.

5/14

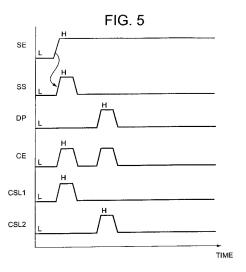
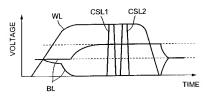


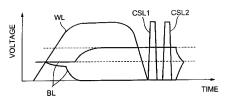
FIG. 6



Sunaga, et. al.

6/14 SECOND SHOT FIRST SHOT ROW ACCESS ROW ACCESS 8 **e** <u>a</u>

FIG. 8



8/14

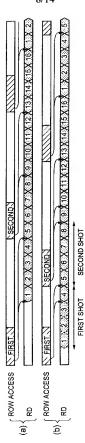


FIG. 9

FIG. 10

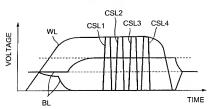
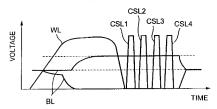


FIG. 11



10/14 FOURTH SHOT THIRD SHOT SECOND SHOT FIRST SHOT ROW ACCESS ROW ACCESS 8 (a) <u>(a)</u>

FIG. 12

JP920030200US1 Sunaga, et. al.

11/14

FIG. 13

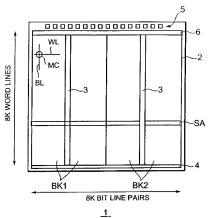


FIG. 14

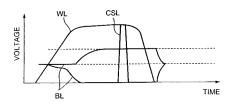


FIG. 15

SHOULD BL 3 3 3 SA1

SK BIT LINE PAIRS

7

13/14

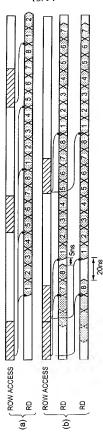


FIG 16

14/14

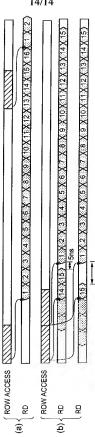


FIG. 17